AMENDMENT TO THE SPECIFATION:

Please delete paragraph [44] and replace it with the following amended paragraph:

[44] To determine the [Period] <u>period</u> (i.e., generate the original clock frequency), the method is initiated and the phase is compared to the horizontal width as illustrated by block 700 and diamond 702A. The phase and horizontal width are represented as number of pixels. If the phase is smaller than HW then the original clock frequency is clocked out based on the period as illustrated by block 706. The process continuously compares the phase to HW to insure proper sync.